

UNIVERSITY OF SOUTHERN CALIFORNIA

COMPUTER ENGINEERING

SCREENING EXAMINATION

EE 552

Asynchronous VLSI Design

All students are assumed to have an excellent knowledge of the major pre-requisite for this class, namely EE 101, i.e. a basic course in logic design. Excellent book for this material include:

1. J. F. Wakerly, **Digital Design, Principles and Practice**, Prentice-Hall, New Jersey, 3rd edition, 2001, ISBN 0-13-089896-1.
2. M. Morris Mano and C. R. Kime, **Logic and Computer Design Fundamentals**, 2nd edition updated, Prentice Hall, 2001, ISBN 0-13-031486-23.

Good sources of material that cover the main topics of EE552 include:

1. P. A. Beerel, R. O. Ozdag, M. Ferretti, **A Designer's Guide to Asynchronous VLSI**, Cambridge University Press, 2010, ISBN13 9780521872447

Please be aware that these references are for guidance in BASIC knowledge. Ph.D. candidates are screened on the basis of talent, course knowledge, independent reading and experience.
